

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Vignia 22313-1450 www.uspto.gov



Rih Data Sheet

CONFIRMATION NO. 9219

DID Data Sneet								
SERIAL NUMBEF 10/799,742	FILING DATE 03/12/2004 RULE	CLASS 365		GROUP ART UNIT 2827		ATTORNEY DOCKET NO. CD03011		
APPLICANTS -CK								
Richard Parent, Shelburne, VT;								
David Chapman, Shelburne, VT;								
** CONTINUING DATA **********************************								
Foreign Priority claimed 35 USC 119 (a-d) conditionet Werified and	Allowance (ter nitials	STATE OR COUNTRY VT	DRA	EETS WING 9	TOTA		INDEPENDENT CLAIMS
ADDRESS Bradley T. Sako WALKER & SAKO, LLP Suite 235 300 South First Street San Jose , CA 95113								
TITLE Method and circuit for reducing defect current from array element failures in random access memories								
FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT FILING FEE No for following: RECEIVED					All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time)			